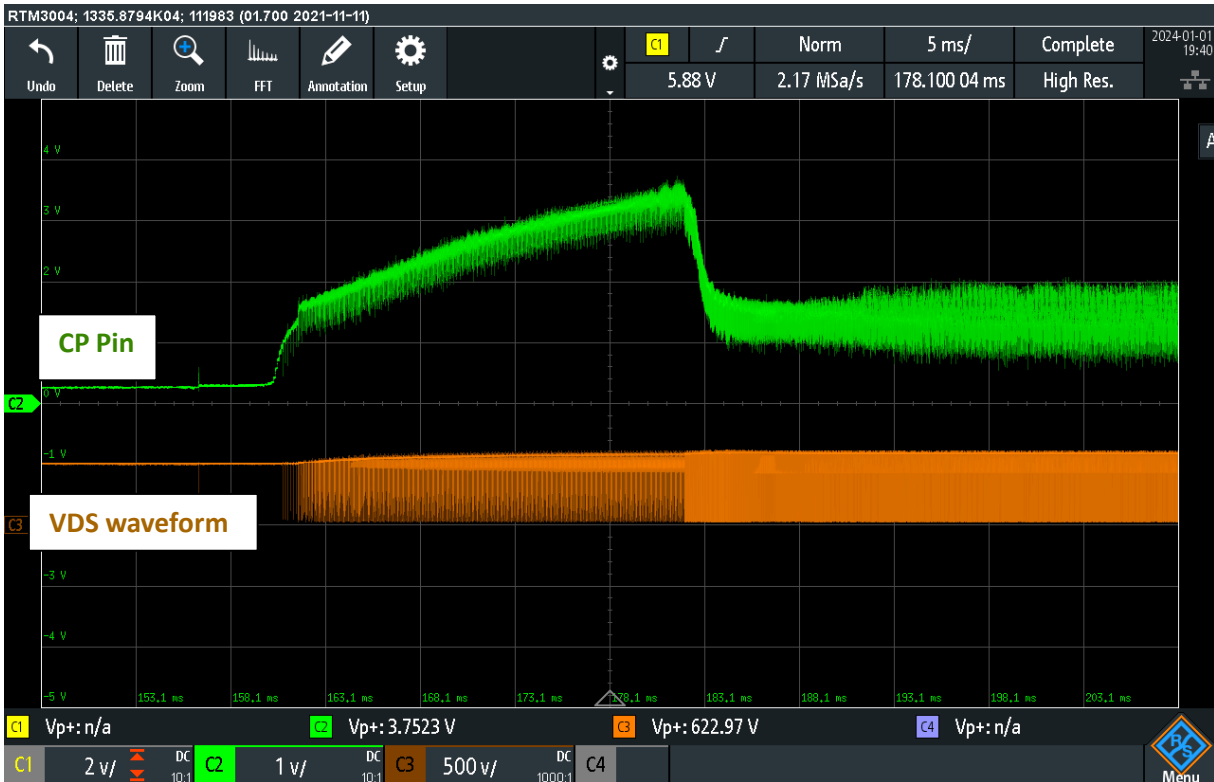
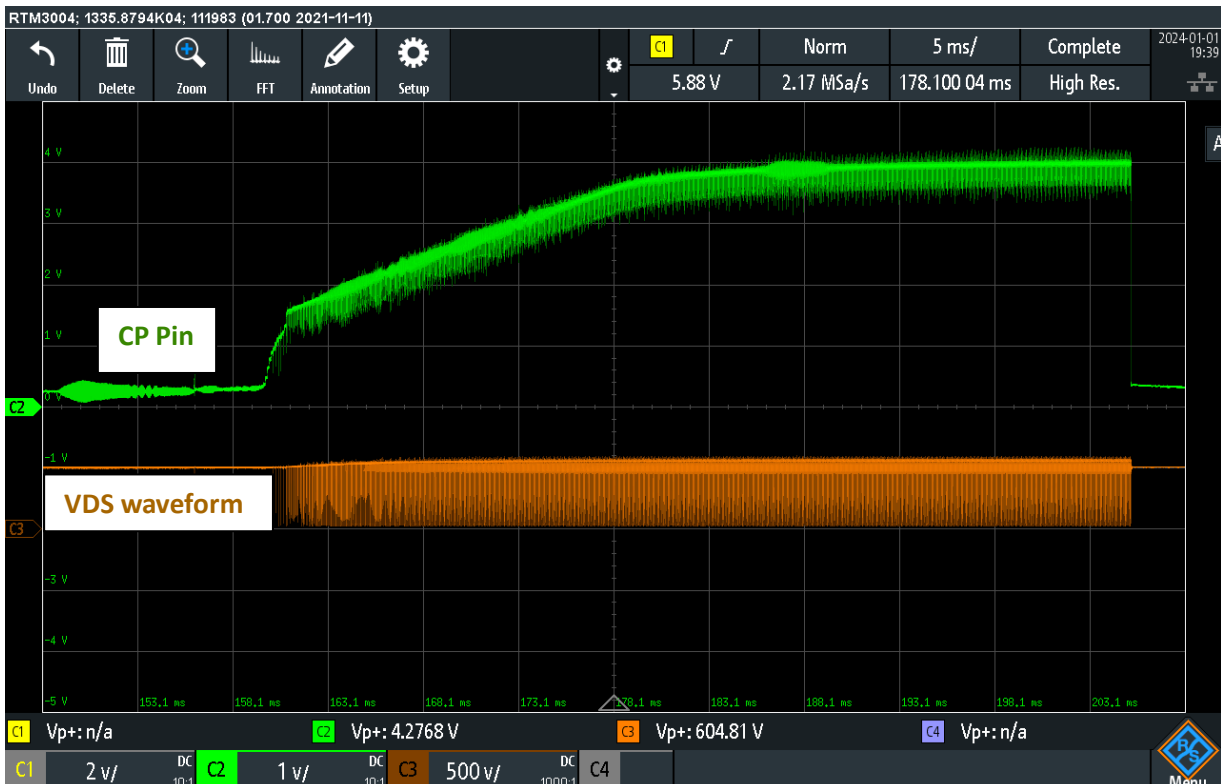


Normal running condition – CP pin value comes down after initial rise. ($V_{in} = 180V$, $P_{in} = 23W$, $P_o = 15W$)



Auto-restart condition – CP pin value keeps rising then comes to zero. ($V_{in} \geq 280V$, $P_{in} = 23W$, $P_o = 15W$)



Other points to be clarified:

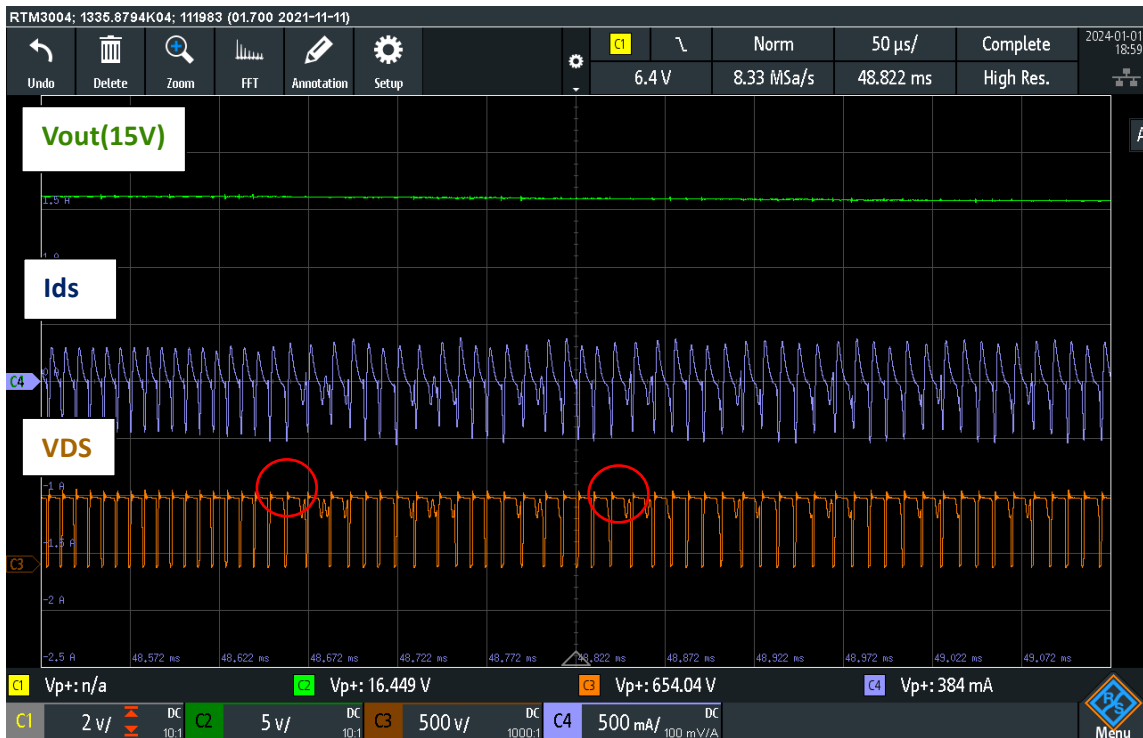
- a) are these switching normal at start-up (Kinda double switching)?



- b) Why does Ids current waveform look abnormal (even when SMPS is working fine) – Current seems to rise during FET off time. [Note – Direction and placement of the probe is correct]



c) Why does VDS waveform has that occasional dip?



Zoomed version –

