

Schematic components that have been frozen by the user will appear with blue reference designators.

Power Supply Input

Var	Value	Units	Description
VDCMIN	100	V	Minimum Input DC Voltage (Manual Overwrite)
VDCMAX	425	V	Maximum Input DC Voltage (Manual Overwrite)
Z	0.47		Loss Allocation Factor
η	95.0	%	Efficiency Estimate (Target) (Manual Overwrite)

Input Section

Var	Value	Units	Description
Fuse	1.25	A	Input Fuse Rated Current
Iavg	0.16	A	Average Diode Bridge Current (DC Input Current)

Device Variables

Var	Value	Units	Description
Device	LNK6763K		PI Device Name
BVDSS	650	V	Drn-Src Bkdn Voltage
Current Limit Mode	Default		Device Current Limit Mode
PO	15.11	W	Total Output Power
VDRAIN Estimated	589.39	V	Estimated Drain Voltage
VDS	4.69	V	On state Drain to Source Voltage (Manual Overwrite)
FS	132000	Hz	Switching Frequency (at VMIN and Full Load)
FMIN_OTE	124000	Hz	Minimum Switching Frequency During On-Time Extension
FMAX_OTE	140000	Hz	Maximum Switching Frequency During On-Time Extension
TSAMPLE_FULL_LOAD	3.67	μ s	Auxiliary Winding Sample Time at Full Load
TSAMPLE_NO_LOAD	3.23	μ s	Auxiliary Winding Sample Time at No Load
KP	0.527		Continuous/Discontinuous Operating Ratio (at VMIN and Full Load)
DMAX	0.486		Maximum Duty Cycle (at VMIN and Full Load)
KI	1.00		Current Limit Reduction Factor
ILIMITEXT	0.72	A	Programmed Current Limit
ILIMITMIN	0.716	A	Minimum Current Limit
ILIMITMAX	0.824	A	Maximum Current Limit
AROTE_FLAG	NO		Auto Restart On-Time Extension Enable
AROTE_ACT	1	ms	Actual Auto Restart On-Time Extension
IP	0.445	A	Peak Primary Current (at VMIN and Full Load)
IRMS	0.233	A	Primary RMS Current (at VMIN and Full Load)
RTH_DEVICE	86.88	$^{\circ}$ C/W	PI Device Heatsink Maximum Thermal Resistance
DEV_HSINK_TYPE	2 Oz (70 μ) 2-Sided Copper PCB		PI Device Heatsink Type

DEV_HSINK_AREA	81	mm ²	PI Device Heatsink Area
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Clamp Circuit

Var	Value	Units	Description
Clamp Type	RCDZ Clamp		Clamp Circuit Type
VCLAMP	74.39	V	Average Clamping Voltage
Estimated Clamp Loss	0.917	W	Clamp total power loss
VC_MARGIN	35.00	V	Clamp Voltage Safety Margin
TPRIMARY	0.99	μs	Primary Drain Voltage Ring Decay Time

Primary Bias Variables

Var	Value	Units	Description
VB	10.0	V	Bias Voltage
IB	0.001	A	Bias Current
PIVB	66	V	Bias Rectifier Maximum Peak Inverse Voltage

Feedback Winding

Var	Value	Units	Description
NFB	9		Feedback Winding Number of Turns
VFB	14.05		Feedback pin voltage
Layers	0.93		Feedback Winding Layers

Transformer Construction Parameters

Var	Value	Units	Description
Core Type	E30 (B66319)		Core Type (Manual Overwrite)
Core Material	N87		Core Material (Manual Overwrite)
Primary Pins	5		Number of Primary pins used
Secondary Pins	4		Number of Secondary pins used
USE_SHIELDS	NO		Use shield Windings
LP_nom	1806	μH	Nominal Primary Inductance
LP_Tol	10.0	%	Primary Inductance Tolerance
NP	67.9		Calculated Primary Winding Total Number of Turns
NSM	12		Secondary Main Number of Turns (Manual Overwrite)
CMA	1374.84	Cmils/A	Primary Winding Current Capacity
VOR	90.00	V	Reflected Output Voltage (Manual Overwrite)
BW	17.50	mm	Bobbin Winding Width
FF	71.09	%	Actual Transformer Fit Factor. 100% signifies fully utilized winding window
TSAMPLE_FULL_LOAD	3.67	μs	Auxiliary Winding Sample Time at Full Load
TSAMPLE_NO_LOAD	3.23	μs	Auxiliary Winding Sample Time at No Load
AE	60.00	mm ²	Core Cross Sectional Area
ALG	392	nH/T ²	Gapped Core Specific Inductance
BM	1971	Gauss	Maximum Flux Density
BP	4018	Gauss	Peak Flux Density
BAC	520	Gauss	AC Flux Density for Core Loss

LG	0.153	mm	Estimated Gap Length
L_LKG	18.06	μH	Estimated primary leakage inductance
LSEC	20	nH	Secondary Trace Inductance

Primary Winding Section 1

Var	Value	Units	Description
NP1	34		Number of Primary Winding Turns in the First Section of Primary
L	0.98		Primary Winding - Number of Layers
DC Copper Loss	0.01	W	Primary Section 1 DC Losses

Primary Winding Section 2

Var	Value	Units	Description
NP2	34		Rounded (Integer) Number of Primary winding turns in the second section of primary
L2	0.98		Primary Number of Layers in 2nd split winding

Output 1

Var	Value	Units	Description
VO	24.00	V	Typical Output Voltage
IO	0.25	A	Output Current
VOUT_ACTUAL	24.23	V	Actual Output Voltage
NS	7		Secondary Number of Turns
L_S_OUT	0.17		Secondary Output Winding Layers
DC Copper Loss	0.03	W	Secondary DC Losses
VD	0.95	V	Output Winding Diode Forward Voltage Drop
VD	0.95	V	Output Winding Diode Forward Voltage Drop
PIVS	142.97	V	Output Rectifier Maximum Peak Inverse Voltage
ISP	0.625	A	Peak Secondary Current
ISRMS	0.337	A	Secondary RMS Current
ISRMS_WINDING	0.337	A	Secondary Winding RMS Current
CMAS	235	Cmils/A	Secondary Winding Current Capacity
RTH_RECTIFIER	225.45	$^{\circ}C/W$	Output Rectifier Heatsink Maximum Thermal Resistance
OR_HSINK_TYPE	2 Oz (70 μ) 2-Sided Copper PCB		Output Rectifier Heatsink Type
OR_HSINK_AREA	52	mm ²	Output Rectifier Heatsink Area
CO	33 x 1	μF	Output Capacitor - Capacitance
IRIPPLE	0.226	A	Output Capacitor - RMS Ripple Current
Expected Lifetime	18175	hr	Output Capacitor - Expected Lifetime

Output 2

Var	Value	Units	Description
VO	15.00	V	Typical Output Voltage
IO	0.50	A	Output Current
VOUT_ACTUAL	15.00	V	Actual Output Voltage
NS	5		Secondary Number of Turns

<i>L_S_OUT</i>	0.17		<i>Secondary Output Winding Layers</i>
<i>DC Copper Loss</i>	0.05	<i>W</i>	<i>Secondary DC Losses</i>
<i>VD</i>	0.90	<i>V</i>	<i>Output Winding Diode Forward Voltage Drop</i>
<i>VD</i>	0.90	<i>V</i>	<i>Output Winding Diode Forward Voltage Drop</i>
<i>PIVS</i>	90.00	<i>V</i>	<i>Output Rectifier Maximum Peak Inverse Voltage</i>
<i>ISP</i>	1.250	<i>A</i>	<i>Peak Secondary Current</i>
<i>ISRMS</i>	0.674	<i>A</i>	<i>Secondary RMS Current</i>
<i>ISRMS_WINDING</i>	1.011	<i>A</i>	<i>Secondary Winding RMS Current</i>
<i>CMAS</i>	375	<i>Cmils/A</i>	<i>Secondary Winding Current Capacity</i>
<i>RTH_RECTIFIER</i>	127.36	<i>°C/W</i>	<i>Output Rectifier Heatsink Maximum Thermal Resistance</i>
<i>OR_HSINK_TYPE</i>	2 Oz (70 µ) 2-Sided Copper PCB		<i>Output Rectifier Heatsink Type</i>
<i>OR_HSINK_AREA</i>	52	<i>mm²</i>	<i>Output Rectifier Heatsink Area</i>
<i>CO</i>	220 x 1	<i>µF</i>	<i>Output Capacitor - Capacitance</i>
<i>IRIPPLE</i>	0.452	<i>A</i>	<i>Output Capacitor - RMS Ripple Current</i>
<i>Expected Lifetime</i>	37575	<i>hr</i>	<i>Output Capacitor - Expected Lifetime</i>

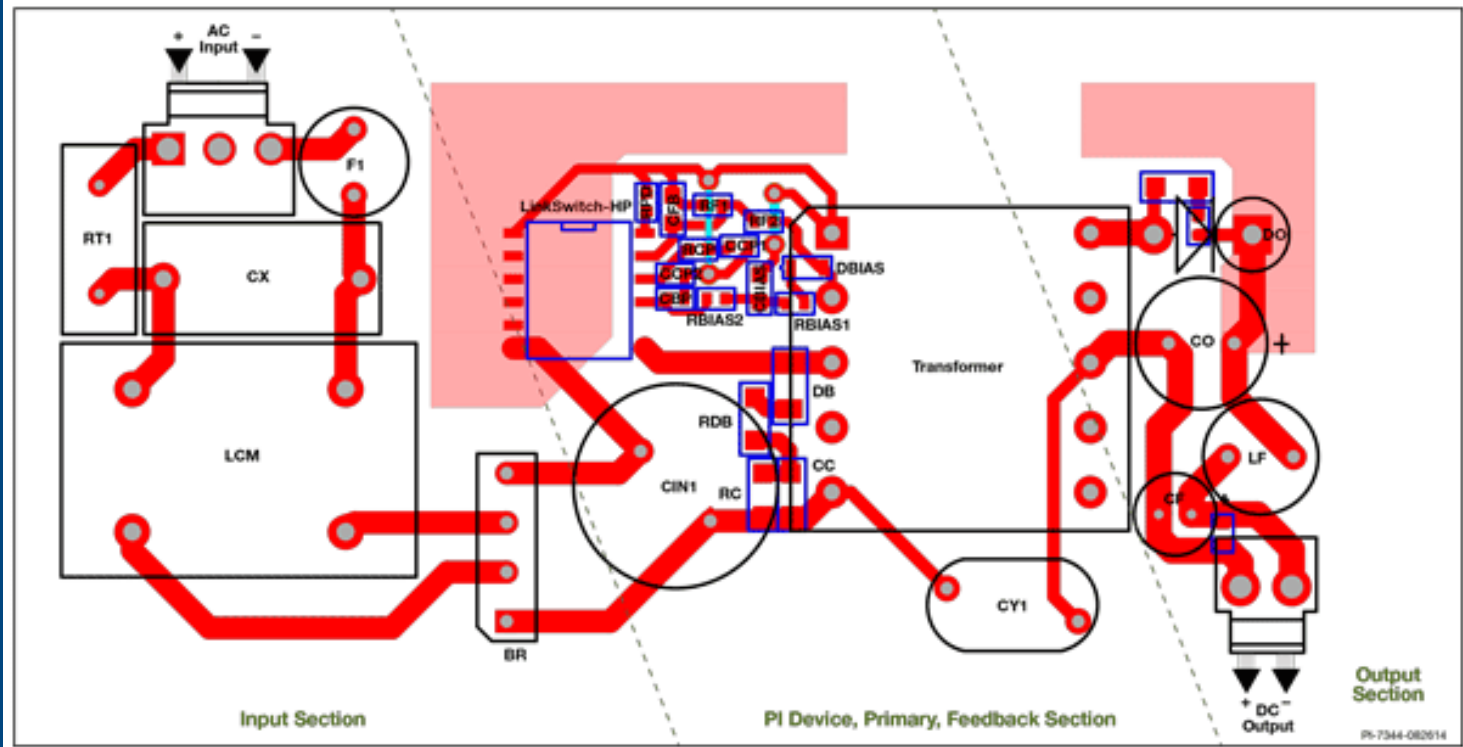
Output 3

Var	Value	Units	Description
<i>VO</i>	8.00	<i>V</i>	<i>Typical Output Voltage</i>
<i>IO</i>	0.20	<i>A</i>	<i>Output Current</i>
<i>VOUT_ACTUAL</i>	8.58	<i>V</i>	<i>Actual Output Voltage</i>
<i>NS</i>	7		<i>Secondary Number of Turns</i>
<i>L_S_OUT</i>	0.26		<i>Secondary Output Winding Layers</i>
<i>DC Copper Loss</i>	0.08	<i>W</i>	<i>Secondary DC Losses</i>
<i>VD</i>	0.70	<i>V</i>	<i>Output Winding Diode Forward Voltage Drop</i>
<i>VD</i>	0.70	<i>V</i>	<i>Output Winding Diode Forward Voltage Drop</i>
<i>PIVS</i>	52.33	<i>V</i>	<i>Output Rectifier Maximum Peak Inverse Voltage</i>
<i>ISP</i>	0.500	<i>A</i>	<i>Peak Secondary Current</i>
<i>ISRMS</i>	0.270	<i>A</i>	<i>Secondary RMS Current</i>
<i>ISRMS_WINDING</i>	1.280	<i>A</i>	<i>Secondary Winding RMS Current</i>
<i>CMAS</i>	1189	<i>Cmils/A</i>	<i>Secondary Winding Current Capacity</i>
<i>RTH_RECTIFIER</i>	432.09	<i>°C/W</i>	<i>Output Rectifier Heatsink Maximum Thermal Resistance</i>
<i>OR_HSINK_TYPE</i>	2 Oz (70 µ) 2-Sided Copper PCB		<i>Output Rectifier Heatsink Type</i>
<i>OR_HSINK_AREA</i>	52	<i>mm²</i>	<i>Output Rectifier Heatsink Area</i>
<i>CO</i>	120 x 1	<i>µF</i>	<i>Output Capacitor - Capacitance</i>
<i>IRIPPLE</i>	0.181	<i>A</i>	<i>Output Capacitor - RMS Ripple Current</i>
<i>Expected Lifetime</i>	27876	<i>hr</i>	<i>Output Capacitor - Expected Lifetime</i>

The regulation and tolerances do not account for thermal drifting and component tolerance of the output diode forward voltage drop and voltage drops across the LC post filter. The actual voltage values are estimated at full load only.

Please verify cross regulation performance on the bench.

Board Layout Recommendations



Click on the "Show me" icon to highlight relevant areas on the sample layout.

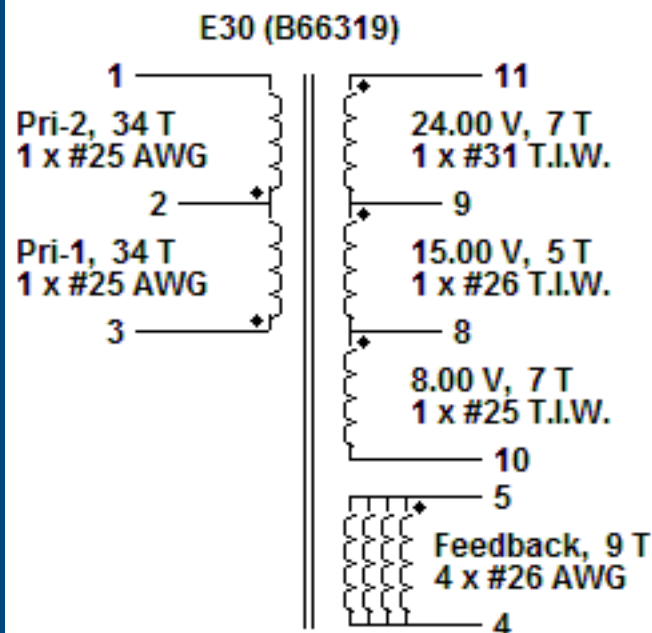
	Description	Show Me
1	Minimize loop area formed by drain, input capacitor and transformer	
2	Minimize loop area formed by secondary winding, the output rectifier and the output filter capacitor	
3	Minimize the loop area formed by the clamp blocking diode, the damping resistor and the snubber capacitor	
4	Place the FB/BP/CP pin components as close to the pin as possible. These signal traces should be routed separately from the power traces. Use of kelvin connection for this purpose is highly recommended.	
5	A large copper area on the cathode of the secondary rectifier is acceptable since this is a quiet node and the larger copper area actually provides heatsinking to the rectifier	
6	The Y capacitor should be placed directly from the primary input filter capacitor positive terminal to the common/return terminal of the transformer secondary	

Bill Of Materials

Ite m #	Quantity	Part Ref	Value	Description	Mfg	Mfg Part Number
1	1	C1	2.2 μ F	2.2 μ F, 450 V, High Voltage Al Electrolytic, (13.5 mm x 10 mm)	Panasonic	EEV-EB2W2R2Q
2	1	C2	0.82 nF	0.82 nF, 1 kV, High Voltage Ceramic	Kemet	C0805C821MDRACTU
3	1	C3	100 nF	100 nF, 16 V, Ceramic, X7R	AVX Corp	0603YC104K4T4A
4	1	C4	100 pF	100 pF, 50 V, Ceramic, C0G	Wurth Elektronik	885012009011
5	1	C5	0.47 μ F	0.47 μ F, 25 V, Ceramic, X7R	Kemet	C0805C474K3RAC7800
6	1	C6	0.1 nF	0.1 nF, 250 VAC, Ceramic, Y Class	Murata	GA342QR7GF101KW01L
7	1	C7	470 pF	470 pF, 200 V, High Voltage Ceramic	Kemet	C0805C471K2RACAUTO
8	1	C8	27 pF	27 pF, 1 kV, High Voltage Ceramic	Murata	GRM31A5C3A270JW01D
9	1	C9	22 pF	22 pF, 630 V, High Voltage Ceramic	Murata	GRM31A5C2J220JW01D
10	1	C10	10 μ F	10 μ F, 50 V, Electrolytic, Gen Purpose, 1000 m Ω , (6.1 mm x 6.3 mm)	Rubycon	50TRV10M6.3X6.1
11	1	C11	10 pF	10 pF, 100 V, Ceramic, C0G	Kemet	K100J15C0GH5TL2
12	1	C12	120 μ F	120 μ F, 16 V, Electrolytic, Super Low ESR, 22 m Ω , (11 mm x 6.3 mm)	United Chemi-Con	EKZE160ELL121MF11D
13	1	C13	100 μ F	100 μ F, 10.0 V, Electrolytic, Low ESR, 500 m Ω , (3.5 mm x 2.8 mm)	Kemet	T495B107M010ATE500
14	1	C14	220 μ F	220 μ F, 25 V, Electrolytic, Super Low ESR, 72 m Ω , (11.5 mm x 8 mm)	United Chemi-Con	EKZE250ELL221MHB5D
15	1	C15	100 μ F	100 μ F, 25 V, Electrolytic, Low ESR, 260 m Ω , (8 mm x 6.2 mm)	Panasonic	EEEFK1E101AP
16	1	C16	33 μ F	33 μ F, 35 V, Electrolytic, Super Low ESR, 300 m Ω , (11 mm x 5 mm)	United Chemi-Con	EKZE350ELL330ME11D
17	1	C17	100 μ F	100 μ F, 35 V, Electrolytic, Low ESR, 80 m Ω , (10.2 mm x 8 mm)	Panasonic	EEE-FP1V101AP
18	1	D1	RGF1M	1000 V, 1 A, Fast Recovery, 500 ns, DO-214AC	ON Semiconductor	RGF1M
19	1	D2	LL4148-M-08	100 V, 0.15 A, Fast Recovery, 8 ns, SOD-80	Vishay	LL4148-M-08
20	1	D3	B160B-13-F	60 V, 1 A, Schottky, DO-214AA	Diodes Inc.	B160B-13-F
21	1	D4	ES2C-E3/52T	150 V, 2 A, Ultrafast Recovery, 30 ns, DO-214AA	Vishay	ES2C-E3/52T
22	1	D5	CRH02(TE85L, Q,M)	200 V, 0.5 A, Ultrafast Recovery, 35 ns, TO-220-3F	Toshiba	CRH02(TE85L,Q,M)
23	1	F1	1.25 A	600 VAC, 1.25 A, Glass Cartridge, Time Lag Fuse	Littelfuse	04611.25ER
24	3	L1, L2, L3	3.3 μ H	3.3 μ H, 2.6 A	Murata	LQH66SN3R3M03L
25	1	R1	10 k Ω	10 k Ω , 5 %, 0.5 W, Thick Film	Generic	
26	1	R2	24 Ω	24 Ω , 5 %, 0.25 W, Thick Film	Generic	
27	1	R3	100 k Ω	100 k Ω , 1 %, 0.25 W, Thick Film	Generic	
28	1	R4	124 k Ω	124 k Ω , 1 %, 0.125 W, Thick Film	Generic	
29	1	R5	2 Ω	2 Ω , 5 %, 0.125 W, Thick Film	Generic	
30	1	R6	22 Ω	22 Ω , 5 %, 0.5 W, Thick Film	Generic	
31	1	R7	390 Ω	390 Ω , 5 %, 0.25 W, Thick Film	Generic	
32	1	R8	470 Ω	470 Ω , 5 %, 0.25 W, Thick Film	Generic	

33	1	R9	3.3 k Ω	3.3 k Ω , 5 %, 0.125 W, Thick Film	Generic	
34	1	R10	56.2 k Ω	56.2 k Ω , 1 %, 0.25 W, Thick Film	Generic	
35	1	R11	12.4 k Ω	12.4 k Ω , 1 %, 0.125 W, Thick Film	Generic	
36	1	T1	E30 (B66319)	3F3 Core Material Refer to Manufacturer datasheet for a number of parts to purchase	EPCOS (TDK)	B66319
37	1	T1 Bobbin	E30/15/7 - 1 (P7-S7)	Bobbin Material : GFR polyterephthalate	EPCOS (TDK)	B66232
38	1	T1 Core Acc.1	B66232	Yoke . Stainless spring steel (0.4mm)	EPCOS (TDK)	B66232
39	1	T1 Core Acc.2	CLA-E30/15/7	Clasp . CuZn Alloy, Ni plated	Ferroxcube	CLA-E30/15/7
40	1	T1 Core Acc.3	SPR-E30/15/7	Spring . Stainless steel (CrNi)	Ferroxcube	SPR-E30/15/7
41	1	U1	LNK6763K	LinkSwitch-HP, LNK6763K, eSOP-12B	Power Integrations	LNK6763K
42	1	VR1	P6SMB110A-E3/52	110 V, 5 W, 5 %, DO-214AA, TVS	Vishay	P6SMB110A-E3/52
43	1			52 mm ² area on Copper PCB. 2 oz (70 μ m) thickness. Heatsink for use with Rectifier D3.	Custom	
44	1			52 mm ² area on Copper PCB. 2 oz (70 μ m) thickness. Heatsink for use with Rectifier D4.	Custom	
45	1			52 mm ² area on Copper PCB. 2 oz (70 μ m) thickness. Heatsink for use with Rectifier D5.	Custom	
46	1			81 mm ² area on Copper PCB. 2 oz (70 μ m) thickness. Heatsink for use with Device U1.	Custom	

Electrical Diagram



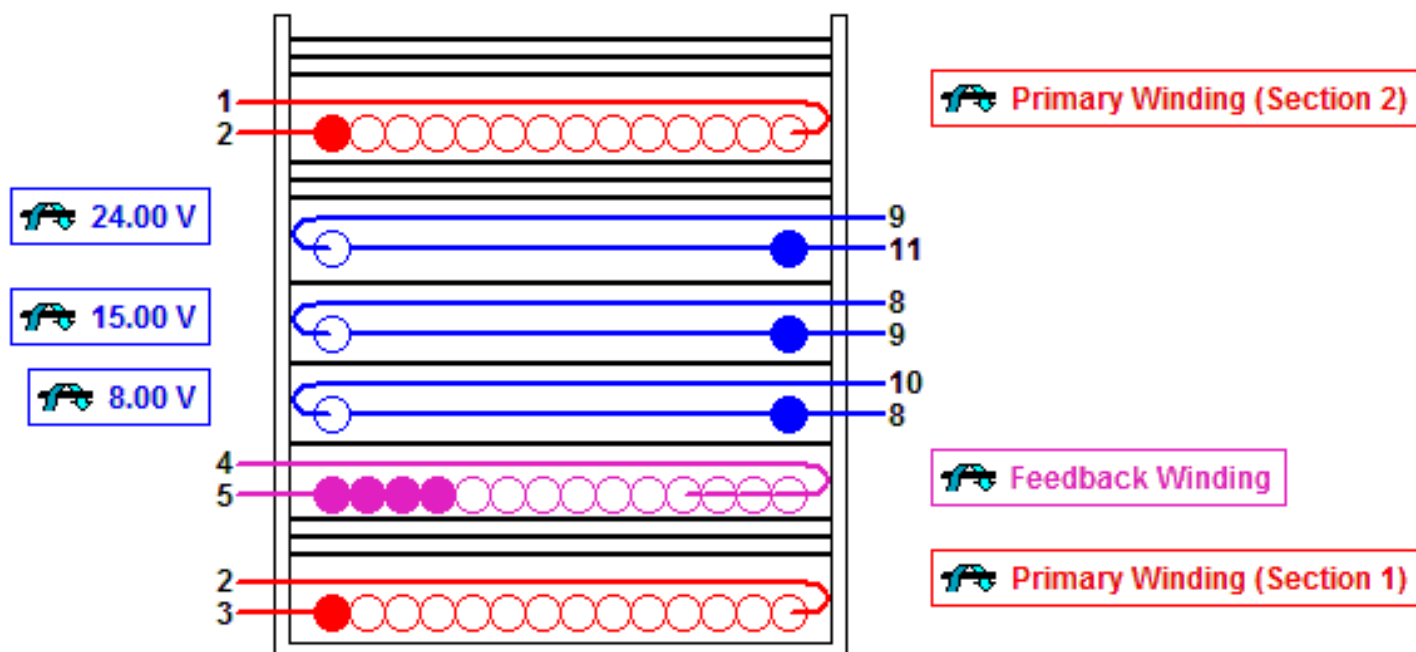
KEY

Pri-1 = Primary Winding (Section 1)

Pri-2 = Primary Winding (Section 2)

T.I.W. = Triple Insulated Wire

Mechanical Diagram



KEY

Blue Dot: Mechanical start of winding (also denotes electrical phase)

Red Arrow: Direction of winding (clockwise)

Winding Instruction

Primary Winding (Section 1)

Start on pin(s) 3 and wind 34 turns (x 1 filar) of item [5], in 1 layer(s) from left to right. Winding direction is clockwise. On the final layer, spread the winding evenly across entire bobbin. Finish this winding on pin(s) 2.

Add 3 layers of tape, item [3], for insulation.

Feedback Winding

Start on any (temp) pin on the secondary side and wind 9 turns (x 4 filar) of item [6]. Winding direction is clockwise. Spread the winding evenly across entire bobbin. Finish this winding on pin(s) 4. Move end of wire from temp pin and terminate it on pin 5.

Add 1 layer of tape, item [3], for insulation.

Secondary Winding

Start on pin(s) 8 and wind 7 turns (x 1 filar) of item [7]. Spread the winding evenly across entire bobbin. Winding direction is clockwise. Finish this winding on pin(s) 10.

Add 1 layer of tape, item [3], for insulation.

Start on pin(s) 9 and wind 5 turns (x 1 filar) of item [8]. Spread the winding evenly across entire bobbin. Winding direction is clockwise. Finish this winding on pin(s) 8.

Add 1 layer of tape, item [3], for insulation.

Start on pin(s) 11 and wind 7 turns (x 1 filar) of item [9]. Spread the winding evenly across entire bobbin. Winding direction is clockwise. Finish this winding on pin(s) 9.

Add 3 layers of tape, item [3], for insulation.

Primary Winding (Section 2)

Start on pin(s) 2 and wind 34 turns (x 1 filar) of item [5]. in 1 layer(s) from left to right. Winding direction is clockwise. On the final layer, spread the winding evenly across entire bobbin. Finish this winding on pin(s) 1.

Add 3 layers of tape, item [3], for insulation.

Core Assembly

Assemble and secure core halves. Item [1].

Varnish

Dip varnish uniformly in item [4]. Do not vacuum impregnate.

Comments

1. Use of a grounded flux-band around the core may improve the EMI performance.
2. For non margin wound transformers use triple insulated wire for all secondary windings.

Materials

Item	Description
[1]	Core: E30 (B66319), 3F3, gapped for ALG of 392 nH/T²
[2]	Bobbin: Generic, 7 pri. + 7 sec.
[3]	Barrier Tape: Polyester film [1 mil (25 µm) base thickness], 17.50 mm wide
[4]	Varnish
[5]	Magnet Wire: 25 AWG (0.45 mm), Solderable Double Coated
[6]	Magnet Wire: 26 AWG (0.4 mm), Solderable Double Coated
[7]	Triple Insulated Wire: 25 AWG (0.45 mm)
[8]	Triple Insulated Wire: 26 AWG (0.4 mm)
[9]	Triple Insulated Wire: 31 AWG (0.22 mm)

Electrical Test Specifications

Parameter	Condition	Spec
Electrical Strength, VAC	60 Hz 1 second, from pins 1,2,3,4,5 to pins 8,9,10,11.	3000
Nominal Primary Inductance, µH	Measured at 1 V pk-pk, typical switching frequency, between pin 1 to pin 3, with all other Windings open.	1806
Tolerance, ±%	Tolerance of Primary Inductance	10.0
Maximum Primary Leakage, µH	Measured between Pin 1 to Pin 3, with all other Windings shorted.	18.06

Although the design of the software considered safety guidelines, it is the user's responsibility to ensure that the user's power supply design meets all applicable safety requirements of user's product.

	Description	Fix	Ref. #
	<i>The clamp safety margin is lower than 40V.</i>	<i>Reduce the design VOR, VC_GOAL or pick a device with a higher BVDSS</i>	652
	<i>The power capability of PI device is assuming that IR reflow soldering is used. If Wave soldering is used, rated power equals V package rated power.</i>	<i>Verify soldering technique and derate if necessary.</i>	182
	<i>Drain voltage close to BVDSS at maximum OV threshold.</i>	<i>Verify BVDSS during line surge, decrease VUVON_MAX or reduce VOR.</i>	237